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A SoC based low power 8-bit flash ADC in 45 nm CMOS technology S. Guha, P. Sharma Student, Dept. of Electronics & Comm. Engg. Surendra Institute of Engineering

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Microelectronics and Electronics (PrimeAsia), 2013 IEEE Asia to conventional flash ADC architecture. Based on SGOI based TFET for low power

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Sudakar Singh Chauhan has received his M.Tech degree in VLSI Design Automation and Technique at NIT Hamirpur in 2009. Since then he has worked in CEERI Pilani in

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RELATED WORK This section tells about the related work done in designing low power TIQ based flash ADC. 12] Sudakar S. Chauhan, S 4-BIT CMOS FLASH ADC USING

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Flash- based 8-bit risc cmos controller

Flash-Based 8-Bit RISC CMOS Controller PT8982/PT8988 DESCRIPTION The PT8982/PT8988 microprocessor is a general purpose 8-bit processor. and low power.

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Abstract: The area of low power and high speed designing of analog-to-digital converters (ADCs) has been a challenging issue over the last decade.

Estimation of low power multi-supply voltage level

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Cmos digital integrated circuits analysis and

CMOS digital integrated circuits analysis and design, A NEW APPROACH TO DESIGN LOW POWER CMOS FLASH A/D CONVERTER by Sudakar S. Chauhan,

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